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VHDL means. Very high speed
integrated circuit hardware
description language. 2.
List the types of HDL? VHDL
and Verilog HDL . 3.
Software used in VHDL lab
is? Xilinx 4. Name any two
programming tools of VHDL?
FPGA, CPLD and ASIC 5. FPGA
means. Field programmable
gate array. 6. CPLD means.

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3. Write HDL code to control speed, direction of DC and Stepper motor. 4. Write HDL code to accept Analog signal, Temperature sensor and display the data on LCD or Seven segment display. 5. Write HDL code to generate different waveforms (Sine, Square, Triangle, Ramp

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