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top
Lecture 1: Circuits & Layout

To realize complex functions of multiple input variables, the basic circuit structures and design principles developed for NOR and NAND can be extended to complex logic gates. The

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ability to realize complex logic functions, using a small number of transistors is one of the most attractive features of nMOS and CMOS logic circuits.

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His research interests include CMOS
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computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has designed chips at Sun Microsystems, Intel, Hewlett-Packard, and Evans & Sutherland.

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Ratios Static CMOS gates are a \square ratioless \square circuit family, meaning that the gates will work cor-rectly for any ratio of PMOS sizes to NMOS sizes. However, the ratios do influence

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switching threshold and delay, so it is important to optimize the P/N ratio for high speed designs. In this section, we will explore the DC transfer characteristics of various ...

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